

WMM7035DBUN0

Bottom port digital silicon Microphone

Descriptions

WMM7035DBUN0 is a Silicon Microphone with digital output and bottom inlet for sound input. It consists of a MEMS sensor and an encoder IC. It converts sensor analog output signal into 1-bit digital PDM data. The digital output format eliminates AC coupling capacitor, reduces RF noise coupling and eases PCB layout requirement.

WMM7035DBUN0 is a cost-effective alternative to traditional electret condenser microphone (ECM). Provided on tap-and-reel, it is ideally suited for high volume applications. And it can be processed directly to customer's PCB using standard automatic pick-and-place equipment and surface mounted via standard solder reflow equipment.

WMM7035DBUN0 can be used to implement the array microphones. Speech quality can be significantly improved by combining two microphones.

The WMM7035DBUN0 is manufactured in a compact 3.50mm*2.65mm*0.98mm, 5-pin package.

Features

- PDM Output
- High SNR
- Multiple performance modes
- Ultra-Stable Performance
- Standard SMD Reflow
- RoHS/Halogen free compliant
- Omnidirectional

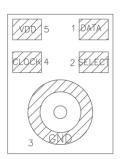
Applications

- Smart phones
 Smart speakers/TV
- ANC-TWS/Headset
- Portable communication device
- Notebook and desktop
- Digital still cameras

Http//:www.willsemi.com



Product appearance



Pin configuration (Bottom view)



Marking (Top view)

Y = Year code

WW = Week code

X X X

X X = Batch code

Order information

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	Device	Package(mm)		Shipping	,
	WMM7035DBUN0-5/TR	3.50*2.65*0.98	5	000 Reel & Take	X
-					TH
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Absolute Maximum Ratings

Parameter	Conditions	Min	Тур	Max	Unit
Supply voltage	VDD to GND	-0.3	-	6.5	V
	L/R,CLOCK,DATA Voltage to GND	-0.4	-	VDD+0.4V	V
Operating Temperature		-40	-	+85	$^{\circ}\mathbb{C}$
Starage Temperature	Solder on PCB	-40	-	+125	$^{\circ}\mathbb{C}$
Storage Temperature	In Tape and Reel	-10	-	+50	$^{\circ}\!\mathbb{C}$

Stresses exceeding these "Absolute Maximum Ratings" could cause permanent damage to the microphone. These are stress rating only. Functional operation at these or any other conditions beyond those indicated under "Absolute and Electrical Characteristics" is not implied. Exposure beyond those indicated under "Acoustic and Electrical Characteristics" for extended periods may affect microphone reliability.





Acoustic & Electrical Specifications

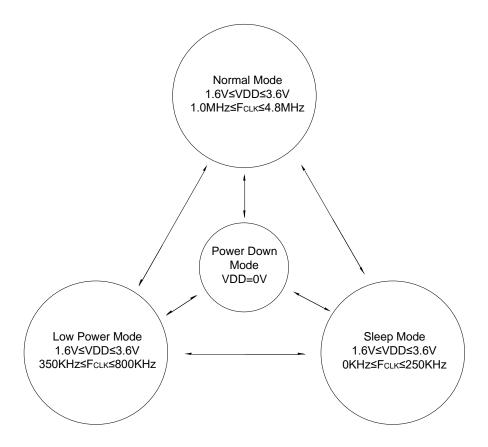
TEST CONDITIONS: $23 \pm 2^{\circ}$ C, $55\pm 20\%$ R.H., VDD=1.8V, Fclock=2.048MHz, Duty Cycle=50%, SELECT pin grounded, no load, unless otherwise indicated.

Param	neter	Symbol	Conditions	Min	Тур	Max	Units
Supply Voltage		V_{DD}		1.6	-	3.6	V
		I _{DD}	Normal operation, Fclk(1MHz~4.8MHz)	1	1	900	uA
Supply (Supply Current		Low power mode, Fclk(350kHz~800kHz)	-	300	-	uA
		I _{sleep}	Sleep mode, Fclk(<250kHz)	-	42	-	uA
	Sleep mode			0	-	250	KHz
Clock	Low power mode			350	-	800	KHz
Frequency Rang	Standard Performance Mode			1	-	4.8	MHz
Sensit	tivity	Sense	94dB SPL @1kHz	-27	-26	-25	dBFS
Cignal to N	oice Detic	SNR	Normal mode 94dB SPL @1kHz, A-weighted	-	65	-	dB(A)
Signal to N	Signal to Noise Ratio		Low power mode 94dB SPL @1kHz, A-weighted	-	64	-	dB(A)
Total Harmonic Distortion		THD	94dB SPL @1kHz, S=Typ	-	0.15	-	%
Acoustic Overload Point		AOP	10%THD @1kHz, S=Typ	-	120	-	dB SPL
Power Supply	y Rejection	PSR+N	100 mVpp square wave @ 217Hz, A-weighted	-	-86	-	dBFS(A)
Power Supply Rat	=	PSRR	200 mVpp sinewave @ 1 kHz	-	65	-	dBv/FS
DC Ou	ıtput	ZOUT	DC fullscale=±100	-	1	-	%FS
Direct	tivity				Omnid	lirectional	
Data Fo	ormat				1/2 Cycl	e 1 bit PDM	
Logic Inp	out High	Vih		0.65x V _{DD}	-	VDD+0 .3	V
Logic Inp	out Low	Vil		-0.3	-	0.35x VDD	V
Logic Out	put High	Voh		VDD- 0.45	-	-	V
Logic Output Low		Vol		-	-	0.45	V
Output	Load	CLOAD		-	-	140	pF
Short Circuit Output Current			94dB SPL @1kHz	1		32里	原MA
Clock Duty Cycle				40	/	\$ 60	A KI
Clock Rise	/Fall Time	TEDGE		-	-	10	ns = 1



- 1. 100% tested.
- 2. IDD varies with CLOAD according to: \triangle IDD = 0.5*VDD* \triangle CLOAD*FCLOCK.
- 3. Maximum specifications are measured at maximum VDD. Typical specifications are measured at standard test Conditions .
- 4. Valid microphones states are: Power Down Mode (mic off), Low Power Mode (mic clock speed), Sleep Mode (low current, DATA = high-Z, fast startup), and Normal Mode (normal operation).
- 5. Time from FCLOCK <250 kHz to ISLEEP specification is met when transitioning from Normal Mode to Sleep Mode.
- 6. Time from FCLOCK ≥ 1 MHz to all applicable specifications are met when transitioning from Sleep Mode to Normal Mode.

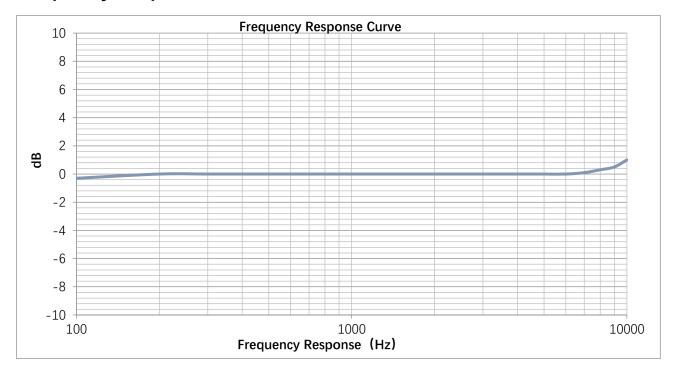
Microphone State Diagram



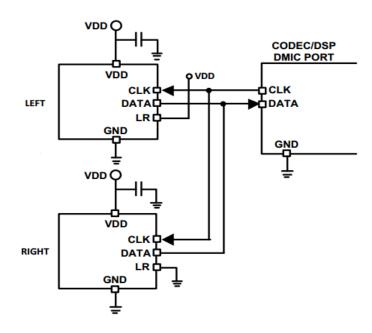




Frequency Response Curve



Application Informations



Microphone	SELECT	Asserts DATA On	Latch DATA On
Mic (High)	V_{DD}	Rising Clock Edge	Falling Clock Edge
Mic (Low)	GND	Falling Clock Edge	Rising Clock Edge

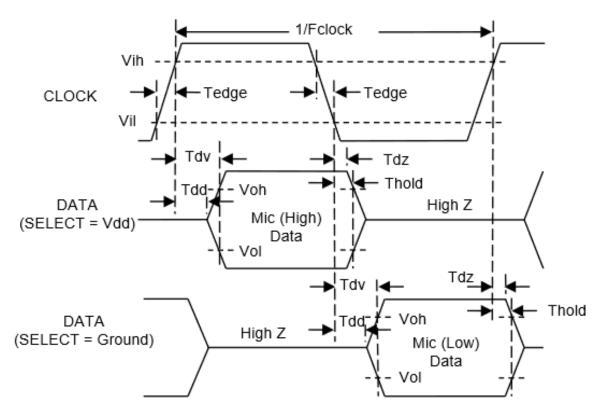
Note:

- All GND pins must be connected to ground.
- Capacitors near the microphone should not contain Class 2 dielectrics.





Clock Timing Diagram



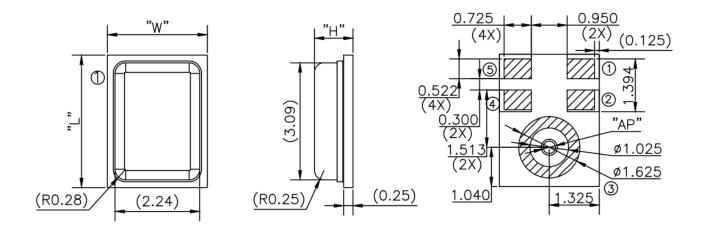
Timing Characteristics

Parameter	Symbol	Min	Тур	Max	Unit	Comments
Low to High Threshold	VI-h	0.65*VDD		VDD+0.3	V	
High to Low Threshold	Vh-l	-0.3		0.35*VDD	V	
DATA into Hi Z Time	Tdz	0		20	ns	RL=1MΩ, CL=12pF
DATA Valid Time	Tdv	24	36	48	ns	RL=1MΩ, CL=12pF
Clock Jitter				0.5	ns	Period jitter in RMS
Clock Duty Cycle		40	50	60	%	
Clock Frequency		350	2400	4800	KHZ	





Mechanical Specification



Item	Dimension	Tolerance
Length(L)	3.50	±0.10
Width(W)	2.65	±0.10
Height(H)	0.98	±0.10
Acoustic Port (AP)	Ø0.325	±0.05

Pin#	Pin Name	Description	
1	1 DATA PDM Output		
2	SELECT	Lo/Hi (L/R) Select	
	SELECT	PDM Output	
3	3 GND GND		
4	CLOCK	Clock input	
5	VDD	Power Supply	

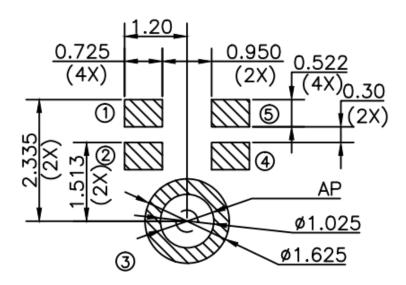
Notes:

- Dimensions are in millimeters unless otherwise specified.
- Tolerance is ±0.10mm unless otherwise specified.
- Pick Area only extends to 0.25 mm of any edge or hole unless otherwise specified.
- Suggestion to use the same date code microphone in one array microphone module.

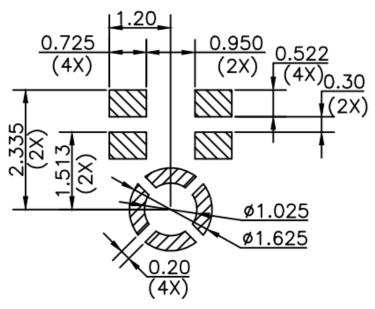




Example Land Pattern



Example Solder Stencil Pattern



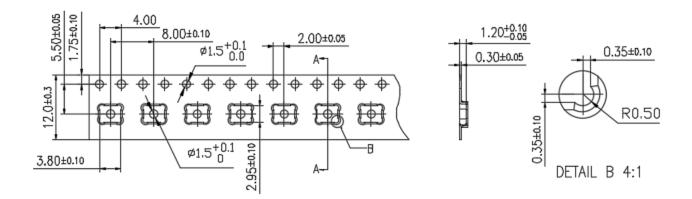
Notes: Dimensions are in millimeters unless otherwise specified.

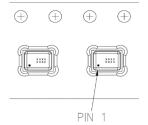
Further optimizations based on application should be performed.





Packaging & Marking Detail





Model Number	Reel Diameter	Quantity Per Reel	
WMM7035DBUN0	13"	5,000	

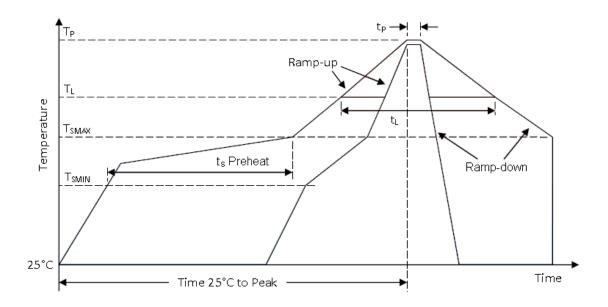
Notes:

- Dimensions are in millimeters unless otherwise specified.
- Vacuum pickup only in the pick area indicated in Mechanical Specifications.
- Tape & reel per EIA-481.
- Labels applied directly to reel and external package.





Referenced Reflow Profile



Profile Feature	Pb-Free
Average Ramp-up rate (Tsmax to Tp)	3°C/second max.
Preheat Temperature Min (Tsmin) Temperature Max (Tsmax) Time (Tsmin to Tsmax) (ts)	150°C 200°C 60-180 seconds
Time maintained above: • Temperature (TL) • Time (tL)	217°C 60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _P)	20-40 seconds
Ramp-down rate (TP to TSMAX)	6°C/second max
Time 25°C to Peak Temperature	8 minutes max

Note:

All temperatures refer to topside of the package, measured on the package body surface.



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Additional Notes

- (A) Maximum of 3 reflow cycles is recommended.
- (B) In order to minimize device damage:
 - Do not board wash or clean after the reflow process.
 - Do not brush board with or without solvents after the reflow process.
 - Do not directly expose to ultrasonic processing, welding, or cleaning.
 - Do not insert any object in port hole of device at any time.
 - Do not apply over 30 psi of air pressure into the port hole.
 - Do not pull a vacuum over port hole of the microphone.
- Do not apply a vacuum when repacking into sealed bags at a rate faster than 0.5 atm/sec.

Materials Statement

Meets the requirements of the European RoHS and Halogen-Free.

Reliability Specifications

Test	Description
Thermal Shock	100 cycles air-to-air thermal shock from -40°C to +125°C with 15 minute soaks.
High Temperature Storage	1000 hours at +105°C environment
Low Temperature Storage	1000 hours at -40°C environment
High Temperature Bias	1000 hours at +105°C under bias.
Low Temperature Bias	1000 hours at -40°C under bias.
Temperature / Humidity Bias	1000 hours at +85°C /85% R.H.
Vibration	4 cycles of 20 to 2,000 Hz sinusoidal sweep with 20g peak
Vibration	acceleration lasting 12 minutes in X, Y, and Z directions.
ESD-HBM	3 discharges of ±2 kV direct contact to I/O pins.
ESD-LID/GND	3 discharges of ±8 kV direct contact to lid while unit is grounded.
ESD-MM	3 discharges of ±200V direct contact to I/O pins.
Reflow	5 reflow cycles with peak temperature of +260°C
Mechanical Shock	3 pulses of 10,000g in the X, Y, and Z direction
Drop Test	To be no interference in operation after dropped to marble or
Diop rest	1.0cm steel plate 18 times from 1.5 meter height.

Note:

After reliability tests are performed, the sensitivity of the microphones shall not deviate more thank from its initial value.