

# REASUNOS

## RSM065040W

### N-Channel SiC Power MOSFET

$V_{DS}$	=	650 V
$R_{DS(on)}$	=	40 m $\Omega$
$I_D@25^\circ\text{C}$	=	72 A

#### Features

- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitance
- Easy to Parallel and Simple to Drive

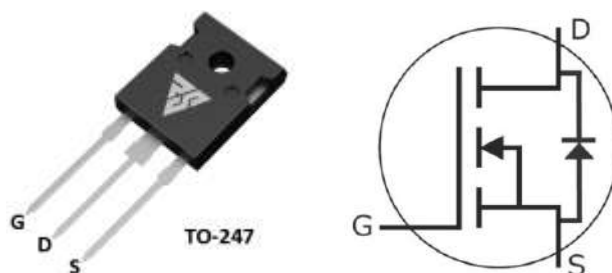
#### Benefits

- Higher System Efficiency
- Reduced Cooling Requirements
- Increased Power Density
- Increased System Switching Frequency

#### Applications

- Power Supplies
- High Voltage DC/DC Converters
- Motor Drives

#### Package



Part Number	Package
RSM065040W	TO-247-3

#### Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{DSmax}$	Drain-Source Voltage	650	V	$V_{GS}=0\text{V}$ , $I_D=1\text{mA}$	
$V_{GSmax}$	Gate-Source Voltage	-10/+25	V	Absolute maximum values	
$V_{GSop}$	Gate-Source Voltage	-5/+20	V	Recommended operational values	
$I_D$	Continuous Drain Current	72	A	$V_{GS}=20\text{V}$ , $T_c=25^\circ\text{C}$	
		58		$V_{GS}=20\text{V}$ , $T_c=100^\circ\text{C}$	
$I_{D(pulse)}$	Pulsed Drain Current	180	A	Pulse width $t_p$ limited by $T_{Jmax}$	
$P_D$	Power Dissipation	348	W	$T_c=25^\circ\text{C}$	
$T_J, T_{STG}$	Operating Junction and Storage Temperature	-55 to +175	$^\circ\text{C}$		

## REASUNOS

## RSM065040W

Electrical Characteristics ( $T_C=25^{\circ}\text{C}$  unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	650			V	$V_{GS}=0V, I_D=1mA$	
$V_{GS(th)}$	Gate Threshold Voltage		3.2		V	$V_{DS}=V_{GS}, I_D=6.1mA$	
			2.2			$V_{DS}=V_{GS}, I_D=6.1mA, T_J=175^{\circ}\text{C}$	
$I_{DSS}$	Zero Gate Voltage Drain Current		3	100	$\mu\text{A}$	$V_{DS}=650V, V_{GS}=0V$	
$I_{GSS}$	Gate-Source Leakage Current		1	100	nA	$V_{DS}=0V, V_{GS}=-5-20V$	
$R_{DS(on)}$	Drain-Source On-State Resistance		40	55	m $\Omega$	$V_{GS}=20V, I_D=20A,$	
			53			$V_{GS}=20V, I_D=20A, T_J=175^{\circ}\text{C}$	
$C_{iss}$	Input Capacitance		2692		pF	$V_{GS}=0V$	
$C_{oss}$	Output Capacitance		179			$V_{DS}=600V$	
$C_{rss}$	Reverse Transfer Capacitance		10.8			$f=1\text{MHz}$	
$E_{oss}$	$C_{oss}$ Stored Energy		35.6			$V_{AC}=25\text{mV}$	
$E_{ON}$	Turn-On Switching Energy		289.1		$\mu\text{J}$	$V_{DS}=400V, V_{GS}=-5V/20V$	
$E_{OFF}$	Turn-Off Switching Energy		117.1			$I_D=20A, R_{G(ext)}=2.5\Omega, L=200\mu\text{H}$	
$t_{d(on)}$	Turn-On Delay Time		24.7		ns	$V_{DS}=400V, V_{GS}=-2V/20V, I_D=30A$ $R_{G(ext)}=3.3\Omega, L=450\mu\text{H}$	
$t_r$	Rise Time		20.3				
$t_{d(off)}$	Turn-Off Delay Time		12.4				
$t_f$	Fall Time		29.6				
$R_{G(int)}$	Internal Gate Resistance		2		$\Omega$	$f=1\text{MHz},$	
$Q_{GS}$	Gate to Source Charge		26.8		nC	$V_{DS}=400V$	
$Q_{GD}$	Gate to Drain Charge		35.7			$V_{GS}=-5V/20V$	
$Q_G$	Total Gate Charge		110.8			$I_D=20A$	

## Reverse Diode Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$V_{SD}$	Diode Forward Voltage	4.0		V	$V_{GS}=0V, I_{SD}=20A$	
		3.6			$V_{GS}=0V, I_{SD}=20A, T_J=175^{\circ}\text{C}$	
$t_{rr}$	Reverse Recover Time	23		ns	$V_R=400V, I_{SD}=30A$ $dif/dt=1700A/\mu\text{s}$	
$Q_{rr}$	Reverse Recovery Charge	161		nC		
$I_{rrm}$	Peak Reverse Recovery Current	10.4		A		

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit	Test Conditions	Note
$R_{\theta JC}$	Thermal Resistance from Junction to Case	0.431		$^{\circ}\text{C/W}$		

# REASUNOS

# RSM065040W

## Typical Performance

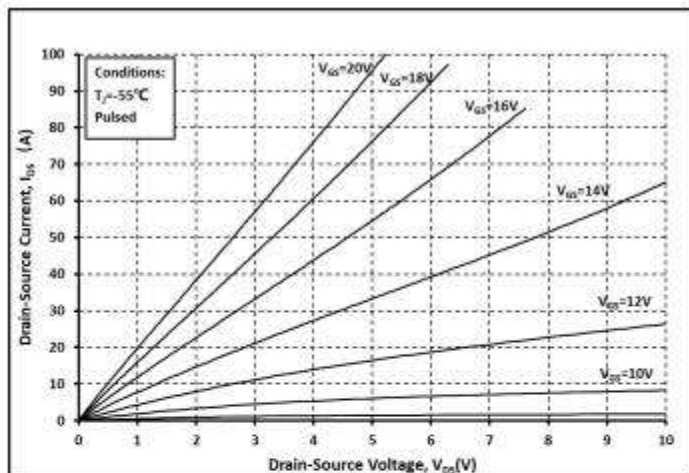


图. 1 输出曲线 @  $T_j = -55^\circ\text{C}$

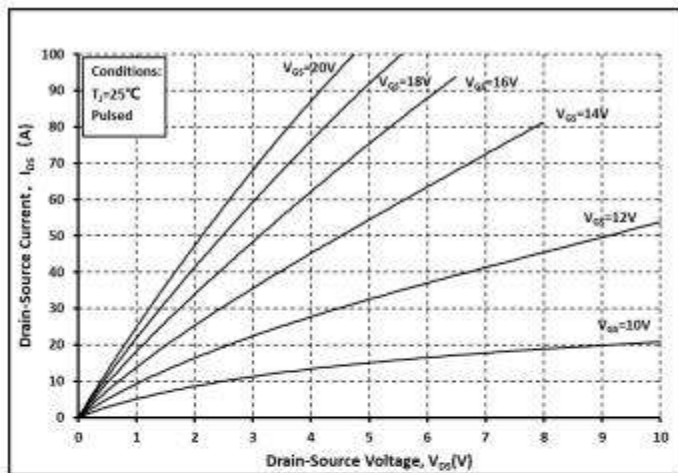


图. 2 输出曲线 @  $T_j = 25^\circ\text{C}$

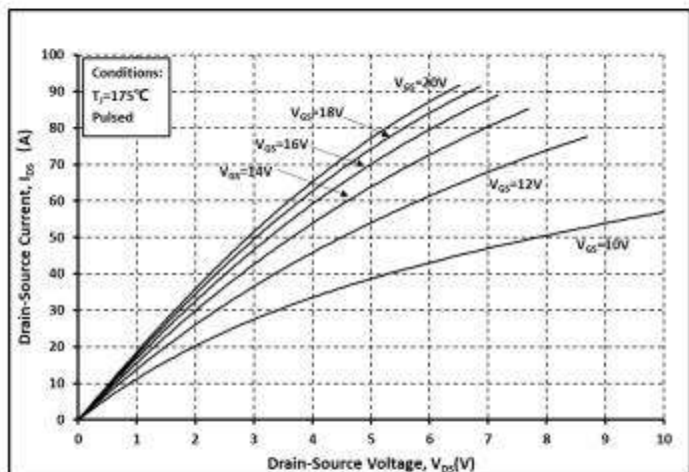


图. 3 输出曲线 @  $T_j = 175^\circ\text{C}$

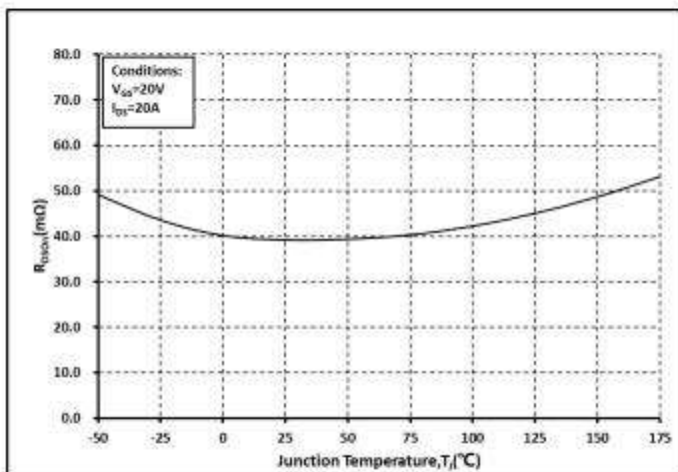


图. 4  $R_{on}$  和温度关系曲线

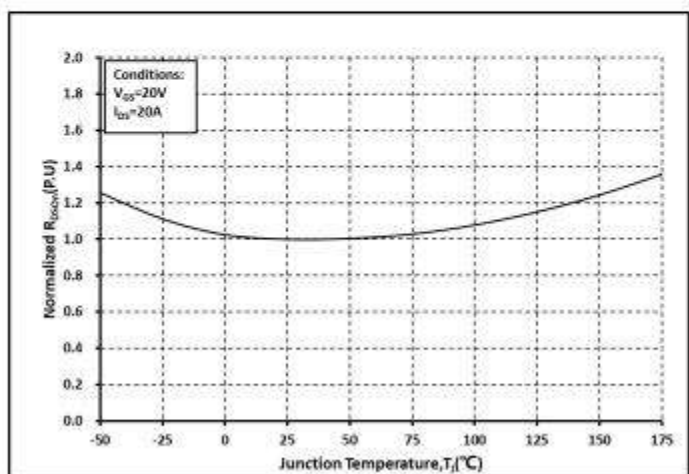


图. 5 归一化的  $R_{on}$  和温度关系曲线

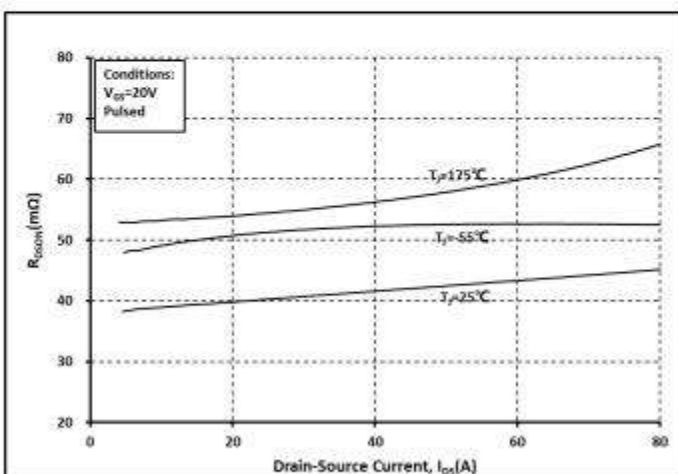


图. 6 各温度下的  $R_{on}$  和  $I_{DS}$  关系曲线

# REASUNOS

# RSM065040W

## Typical Performance

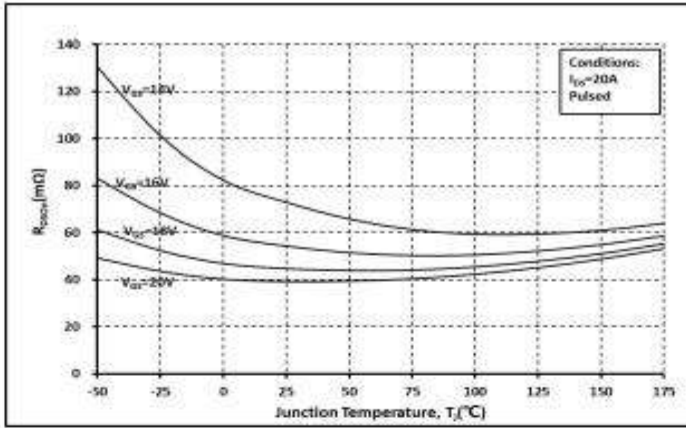


图. 7 各  $V_{GS}$  下的  $R_{on}$  和温度关系曲线

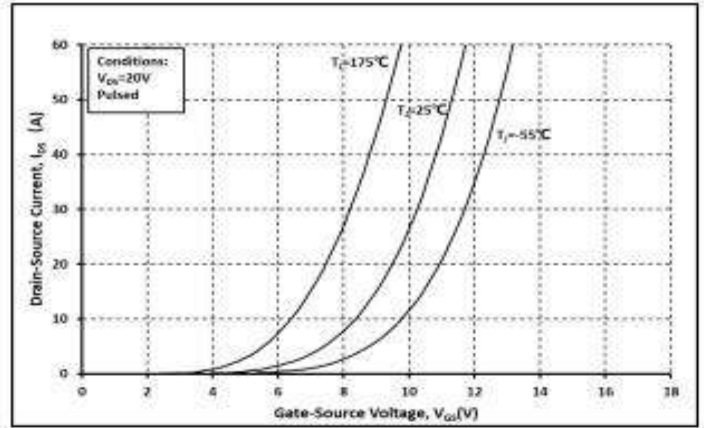


图. 8 各温度下的传输特性曲线

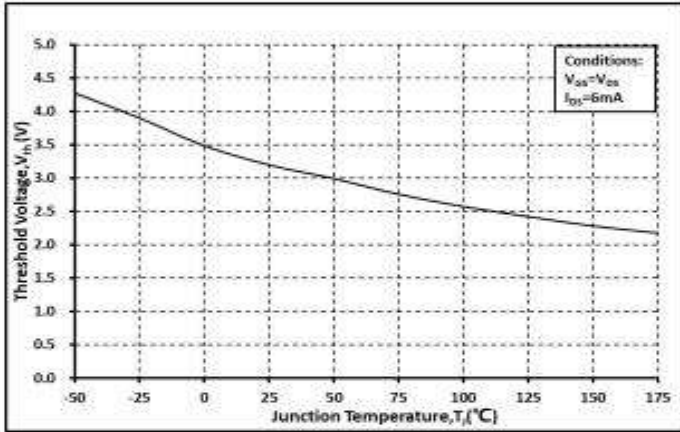


图. 9 阈值电压随温度变化曲线

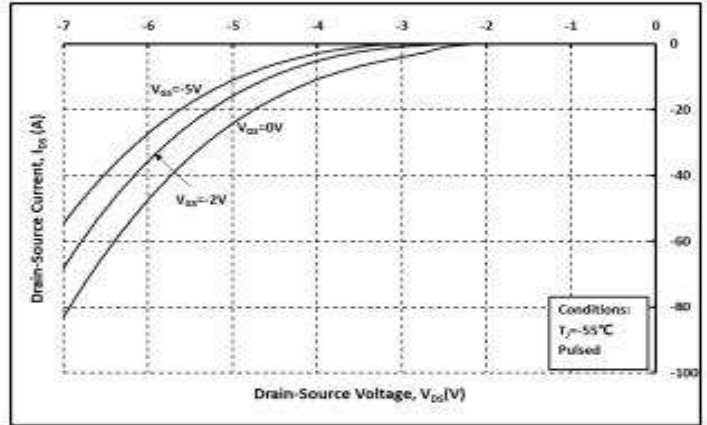


图. 10 体二极管导通曲线 @  $T_j = -55^\circ\text{C}$

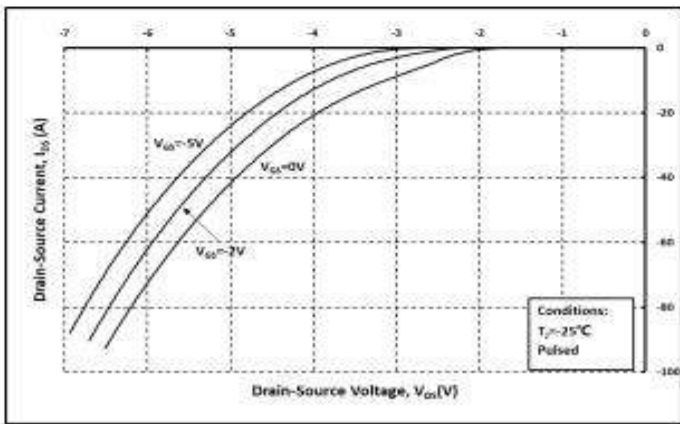


图. 11 体二极管导通曲线 @  $T_j = 25^\circ\text{C}$

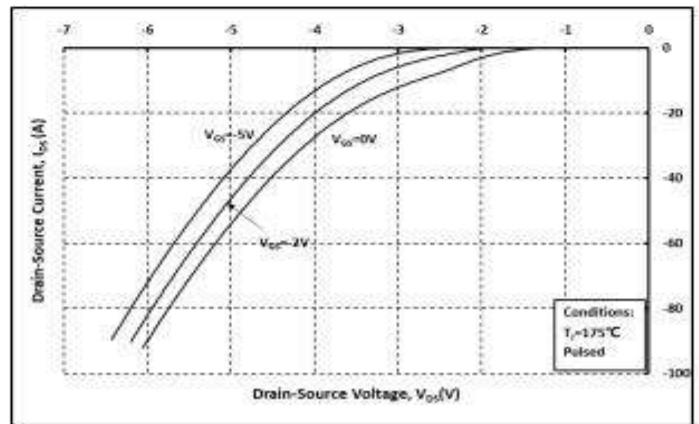


图. 12 体二极管导通曲线 @  $T_j = 175^\circ\text{C}$

# REASUNOS

## RSM065040W

### Typical Performance

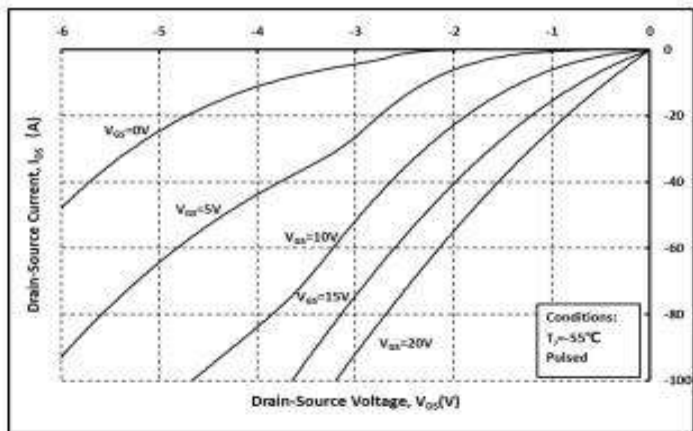


图. 13 第三象限曲线 @  $T_j = -55^\circ\text{C}$

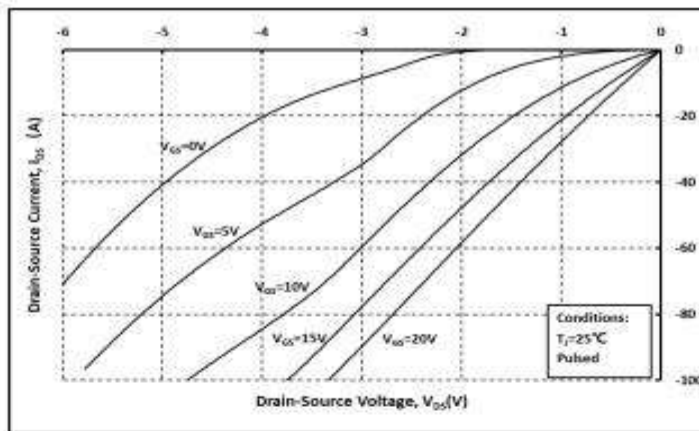


图. 14 第三象限曲线 @  $T_j = 25^\circ\text{C}$

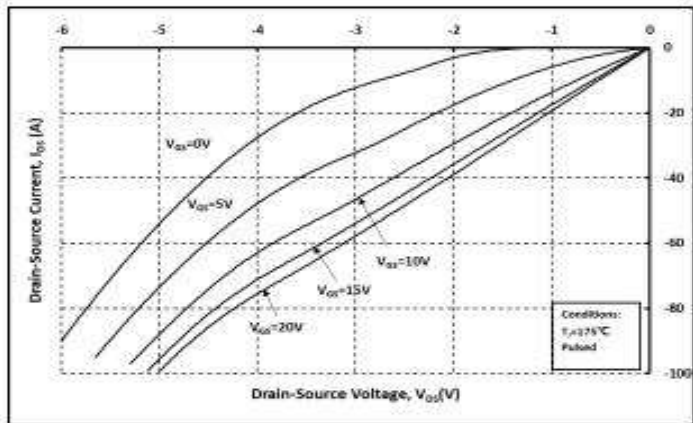


图. 15 第三象限曲线 @  $T_j = 175^\circ\text{C}$

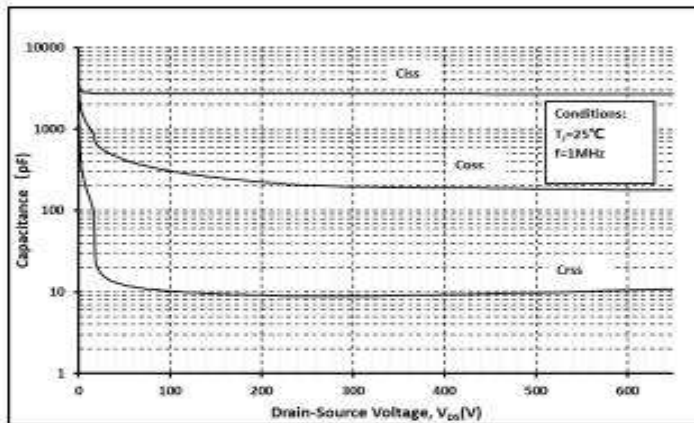


图. 16 各电容和  $V_{DS}$  关系曲线

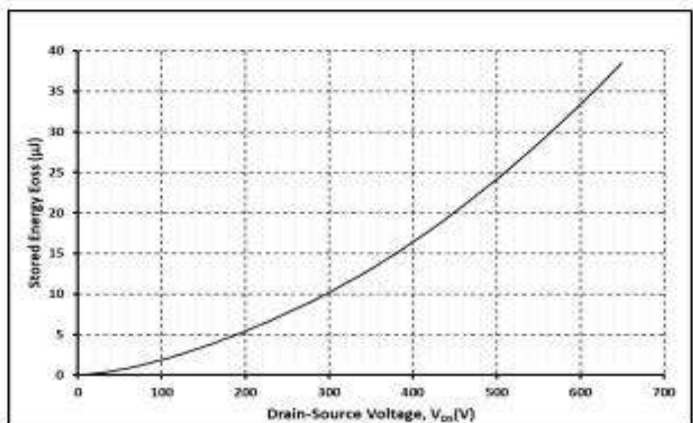


图. 17 输出电容存储能量曲线 c

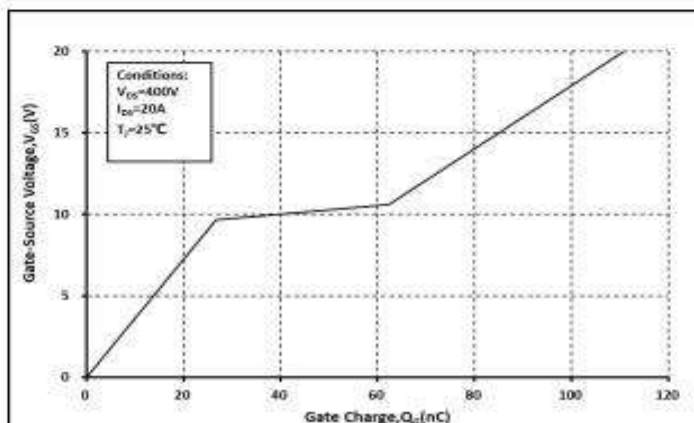


图. 18 栅电荷特征曲线 c

# REASUNOS

# RSM065040W

## Typical Performance

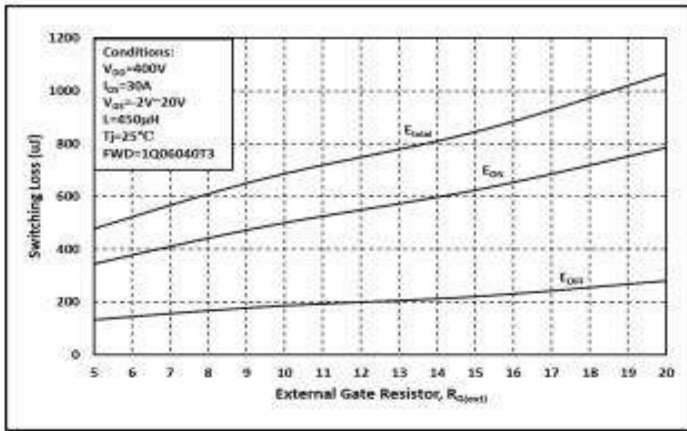


图. 19 开关能量和栅极电阻  $R_{G(EXT)}$  关系曲线

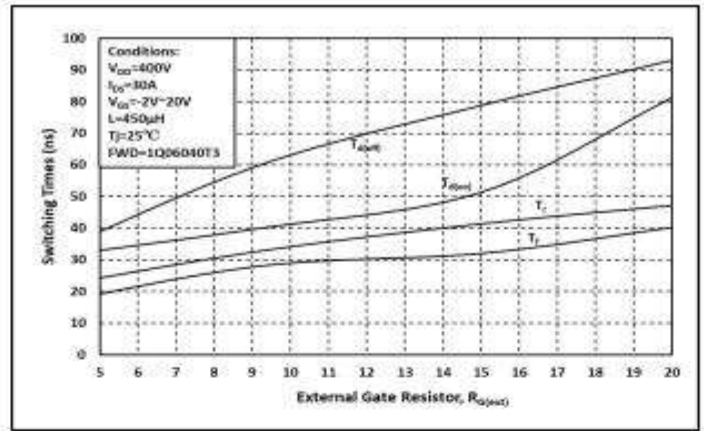


图. 20 开关时间和栅极电阻  $R_{G(EXT)}$  关系曲线

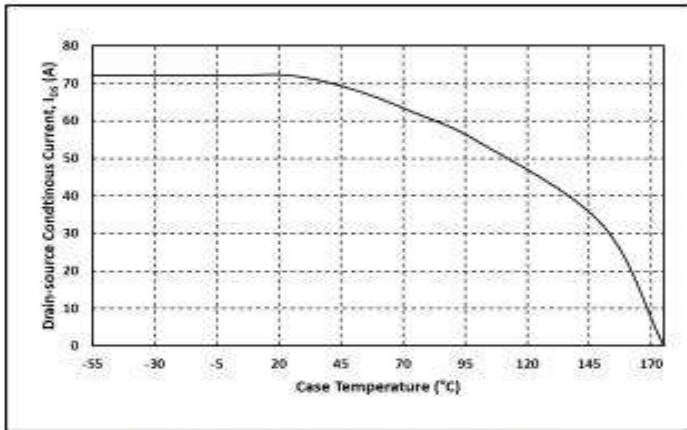


图. 21 漏端电流和温度关系曲线

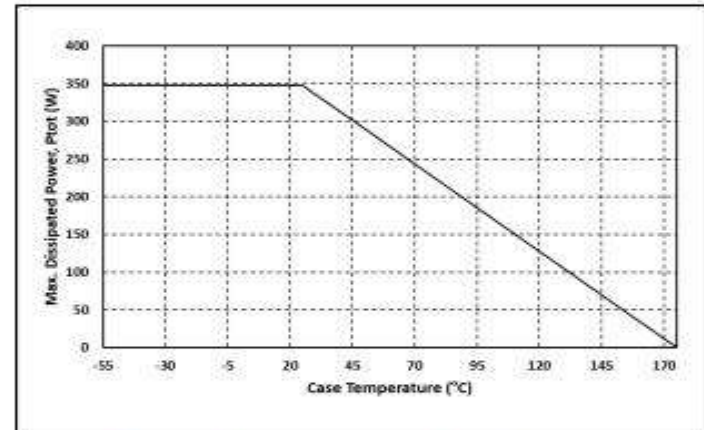


图. 22 最大功耗降额和温度关系曲线

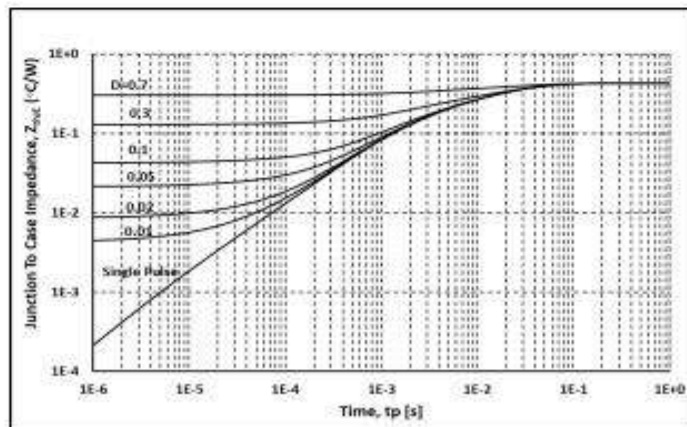


图. 23 热阻曲线

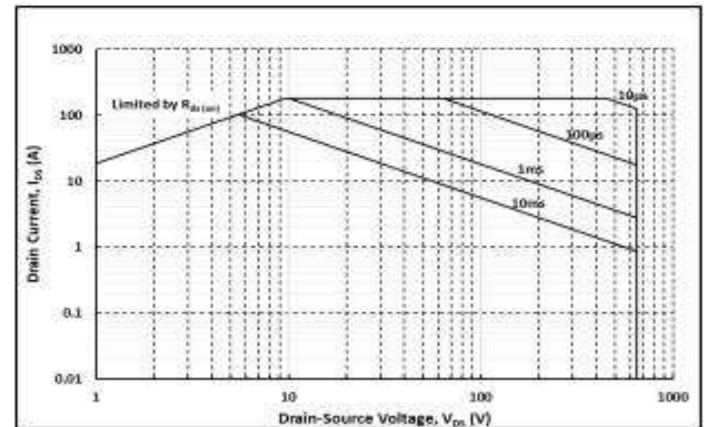


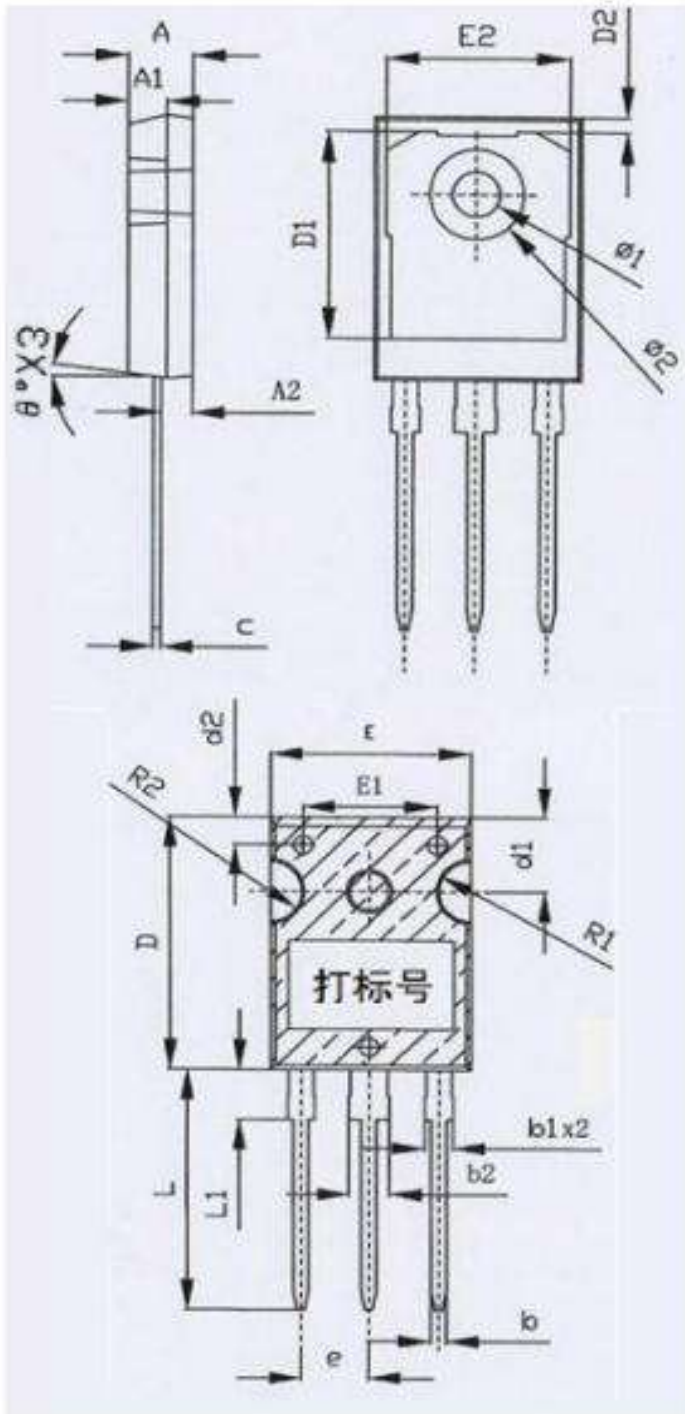
图. 24 安全工作区示意图

# REASUNOS

## RSM065040W

### Package Dimensions

Package TO-247-3



SYMBOLS	DIMENSIONS IN MILLIMETERS		
	MIN	NOM	MAX
A	4.9	5	5.1
A1	2.9	3	3.1
A2	2.31	2.36	2.41
b	1.16	1.2	1.26
b1	2.05	-	2.2
b2	3.05	-	3.2
c	0.58	0.6	0.66
D	20.9	21	21.1
D1	16.46	16.56	16.76
D2		1.17	
d1	6.05	6.15	6.25
d2	2.2	2.3	2.4
E	15.7	15.8	15.9
E1		10.5	
E2		14.02	
e	-	1.27bcs	-
L	19.82	19.92	20.02
L1	1.88	1.98	2.08
θ	0°	7°	8°
R1	-	2.7	-
R2	-	2.5	-
φ1		3.6	
φ2	-	7.19	-

### Disclaimers:

Reasunos Semiconductor Technology CO.,LTD(Reasunos)reserves the right to make changes without notice in order to improve reliability,function or design and to discontinue any product or service without notice .Customers should obtain the latest relevant information before orders and should verify that such information in current and complete.All products are sold subject to Reasunos's terms and conditions supplied at the time of orderacknowledgement.

Reasunos Semiconductor Technology CO.,LTD warrants performance of its hardware products to the specifications at the time of sale.Testing,reliability and quality control are used to the extene Reasunos deems necessary to support this warrantee. Except where agreed upon by contractual agreement,testing of all parameters of each product is not necessarily performed.

Reasunos Semiconductor Technology CO.,LTD does not assume any liability arising from the use of any product or circuit designs described herein.Customers are responsible for their products and applications using Reasunos's components.To minimize risk,customers must provide adequate design and operating safeguards.

Reasunos Semiconductor Technology CO.,LTD does not warrant or convey any license either expressed or implied under its patent rights,nor the rights of others.Reproduction of information in Reasunos's data sheets or data books is permissible only if reproduction is without modification oralteration.Reproduction of this information with any alteration is an unfair and deceptive business practice. Reasunos Semiconductor Technology CO.,LTD is not responsible or liable for such altered documentation.

Resale of Reasunos's products with statements different from or beyond the parameters stated by Reasunos Semiconductor Technology CO.,LTD for that product or service voids all express or implied warranties for the associated Reasunos's product or service and is unfair and deceptive business practice. Reasunos Semiconductor Technology CO.,LTD is not responsible or liable for such statements.

---

### Life Support Policy:

Reasunos Semiconductor Technology CO.,LTD's Products are not authorized for use as critical components in life support devices or systems without the expressed written approval of Reasunos Semiconductor Technology CO.,LTD.

As used herein:

1. Life support devices or systems are devices or systems which:
    - a.are intended for surgical implant into the human body,
    - b.support or sustain life,
    - c.whose failuer to when properly used in accordance with instructions for used provided in the laeling,can be reasonably expected to result in significant injury to the user.
  
  - 2.A critical component is any component of a life support device or system whose failure to system whose failure to perform can be reasonably expected to cause the failure of the life support device or system,or to affect its safety or effectiveness.
-