2.Drain

3.Source





## Lead Free Package and Finish

#### **Applications:**

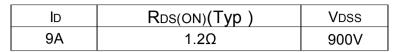
- •Switch Mode Power Supply(SMPS)
- Uninterruptible Power Supply(UPS)
- Power Factor Correction (PFC)

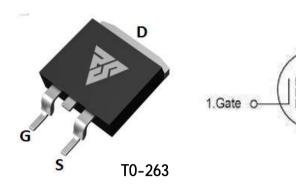
# Features:

- · Fast switching speed
- 100% avalanche tested
- · Improved dv/dt capability

# Ordering Information

Part Number	Package	Marking
RS9N90S	TO-263	RS9N90S





Not to Scale

#### Absolute Maximun Ratings Tc=25 unless otherwise specified

Symbol	Parameter	RS9N90S	Units
VDSS	Drain-to-Source Voltage	900	V
ID	Continuous Drain Current	9	A
IDM	Pulsed Drain Current (Note*1)	36	
PD	Power Dissipation	178	W
VGS	Gate-to-Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L=10mH VDD=50V RG=25Ω TJ=25	245	mJ
IAS	Avalanche Current (Note*1)	7	A
Ear	Repetitive Avalanche Energy (Note*1)	0.98	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

<sup>\*</sup> Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.

#### Thermal Resistance

Symbol	Parameter	RS9N90S	Units	Test Conditions
RθJC	Junction-to-Case	0.7		Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of +150
RθJA	Junction-to-Ambient	62.5		1 cubic foot chamber, free air.



# OFF Characteristics TJ=250 unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain-to-source Breakdown Voltage	900			V	Vgs=0V,ID=250µA
IDSS	Drain-to-Source Leakage Current			1.0	μA	VDS=900V,VGS=0V
loco	Gate-to-Source Forward Leakage			100	n 1	VGS=+30V, VDS=0V
IGSS	Gate-to-Source Reverse Leakage			- 100	nA	Vgs=-30V ,Vds=0V

# ON Characteristics TJ=25C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain-to-Source On-Resistance (Note*2)		1.2	1.55	Ω	Vgs=10V,ID=4.5A
Vgs(TH)	Gate Threshold Voltage	3.0		4.0	V	VGS=VDS,ID=250µA

# Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn-on Delay Time		46			
trise	Rise Time		35		nS	VDS=450V ID=9A RG=25Q
td(OFF)	Turn-OFF Delay Time		317			
tfall	Fall Time		56			

## Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		1514			Vgs=0V
Coss	Output Capacitance		150		pF	Vps=25V
Crss	Reverse Transfer Capacitance		32			f=1.0MHz
Qg	Total Gate Charge		64			Vps=720V
Qgs	Gate-to-Source Charge		7		nC	ID=9A
Qgd	Gate-to-Drain("Miller") Charge		34			VGS=10V

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#### Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Is	Continuous Source Current			9	Α	Integral pn-diode
Ism	Maximum Pulsed Current		-	36	Α	in MOSFET
VsD	Diode Forward Voltage			1.4	V	Is=4.5A,VGS=0V
trr	Reverse Recovery Time		298		nS	Vgs=0V
Qrr	Reverse Recovery Charge		1.7		μC	Is=9A,di/dt=100A/μs

#### Notes:

- \*1. Repetitive rating; pulse width limited by maximum junction temperature.
- \*2. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

# **Typical Feature curve**

Figure 1. Output Characteristics (T<sub>J</sub> = 25°C)

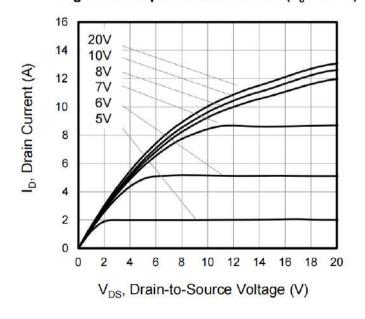
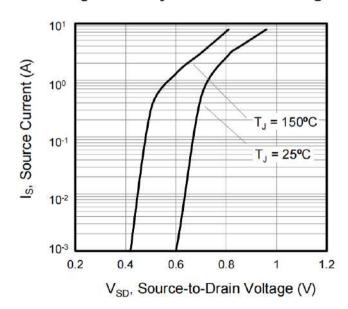


Figure 2. Body Diode Forward Voltage



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Figure 3. Drain Current vs. Temperature

(Y) Tuesday 10

(Y) T

Figure 4. BV<sub>DSS</sub> Variation vs. Temperature

1.2  $V_{GS} = 0V$   $I_D = 250 \,\mu\text{A}$ 1.1 0.9 -50  $T_J, Junction Temperature (°C)$ 

Figure 5. Transfer Characteristics

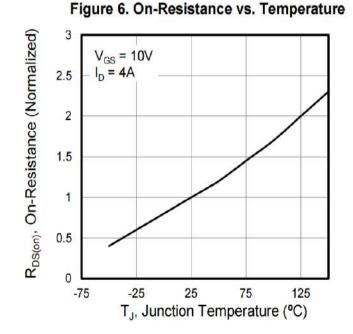
T<sub>J</sub> = 25°C

T<sub>J</sub> = 150°C

T<sub>J</sub> = 150°C

T<sub>J</sub> = 150°C

V<sub>GS</sub>, Gate-to-Source Voltage (V)



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0

Figure 7. Capacitance

10000

Coss

1000

Coss

100

Coss

100

Coss

100

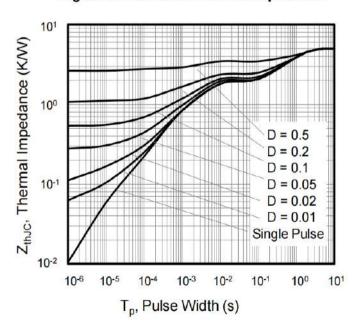
Coss

100

Toss

Figure 8. Gate Charge 10 V<sub>GS</sub>, Gate-to-Source Voltage (V)  $V_{DD} = 180V$ V<sub>DD</sub> =450V 8  $V_{DD} = 720V$ 6 4 2 0 0 20 40 60 Q<sub>a</sub>, Total Gate Charge (nC)

Figure 9. Transient Thermal Impedance



25

## **Test Circuits and Waveforms**

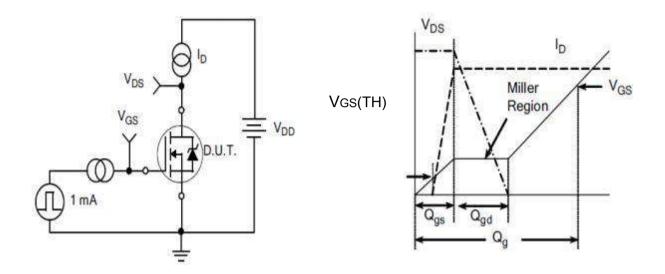


Figure A.
Gate Charge Test Circuit

Figure B.
Gate Charge Waveform

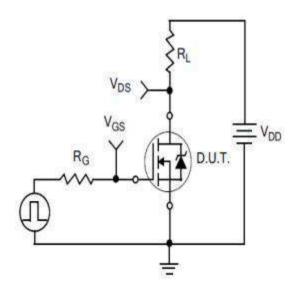


Figure C.
Resistive Switching Test Circuit

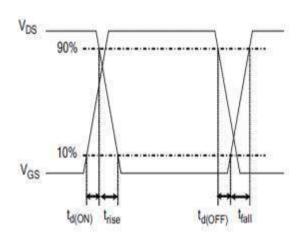


Figure D. Resistive Switching Waveforms



# **Test Circuits and Waveforms**

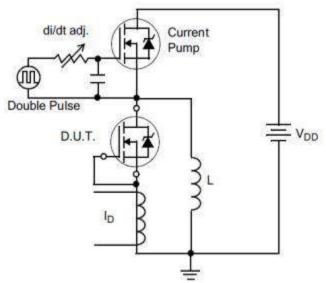


Figure E.Diode Reverse Recovery
Test Circuit

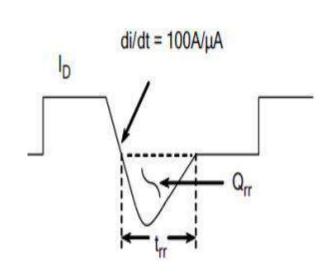


Figure F.Diode Reverse Recovery Waveform

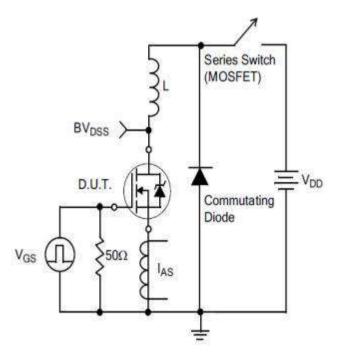


Figure G.Unclamped Inductive Switching Test Circuit

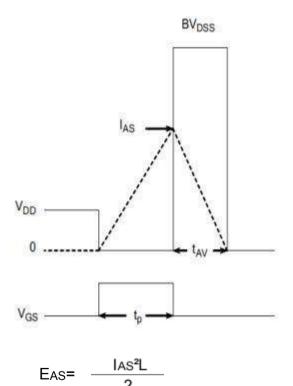


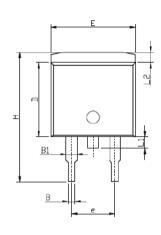
Figure H.Unclamped Inductive Switching Waveforms

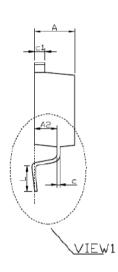
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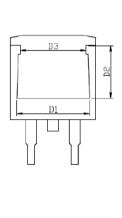
# Package outline drawing

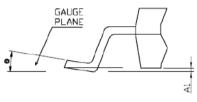
# TO-263

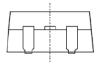
Unit:mm











VIEW1

CVAADOLC	MILLIMETERS					
SYMBOLS	MIN	MAX				
Α	4.40	4.90				
A1	0.05	0.30				
A2	2.40	2.80				
В	0.72	0.92				
B1	1.12	1.45 0.48 1.37 8.86				
С	0.28					
c1	1.17					
D	8.46					
D1	7.90	8.40				
D2	5.50	5.90				
D3	7.10	7.50				
E	9.85	10.45				
e	5.08	BCS				
Н	14.75	15.55				
L	2.30	2.80				
L1	1.20	1.60				
L2	1.01	1.50				



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