

N Channel MOSFET

Applications:

- Adapter & Charger
- •SMPS Standby Power
- •AC-DC Switching Power Supply
- •LED driving power

Features:

- Low On Resistance
- Low Gate Charge
- •Peak Current vs Pulse Width Curve
- •RoHS Compliant

Ordering Information

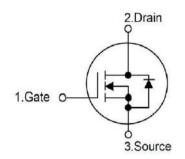
Part Number	Package	Marking
RS8N65F	TO-220F	RS8N65F



Lead Free Package and Finish

lp	RDS(ON)(Typ.)	VDSS
8.0A	1.0Ω	650V





Not to Scale

Absolute Maximun Ratings Tc=25℃ unless otherwise specified

Symbol	Parameter	RS8N65F	Units
VDSS	Drain-to-Source Voltage (Note*1)	650	V
ID	Continuous Drain Current	8.0	
ID@ 100 ℃	Continuous Drain Current	5.5	Α
lом	Pulsed Drain Current (Note*2)	32.0	7
DD	Power Dissipation	48	W
PD	Derating Factor above 25℃	0.38	W/℃
VGS	Gate-to-Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy L=10mH VDD=50V RG=25Ω TJ=25℃	311	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds Package Body for 10 seconds	300 260	$^{\circ}$
TJ and TSTG	Operating Junction and Storage	-55 to 150	1
10 4114 1010	Temperature Range	22.0 100	

^{*}Drain Current Limited by Maximum Junction Temperature

Caution:Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.

Thermal Resistance

Symbol	Parameter	RS8N65F	Units	Test Conditions
Rejc	Junction-to-Case	2.72	°C/W	Drain lead soldered to water cooled heatsink,PD adjusted for a peak junction temperature of +150℃.
Røja	Junction-to-Ambient	120		1 cubic foot chamber,free air.

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OFF Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain-to-source Breakdown Voltage	650			V	Vgs=0V,ID=250µA
IDSS	Drain-to-Source Leakage Current			1.0	μΑ	V _{DS} =650V,VGS=0V
Igss	Gate-to-Source Forward Leakage			100	nΛ	Vgs=+30V Vds=0V
1633	Gate-to-Source Reverse Leakage			-100	nA	Vgs=-30V Vds=0V

ON Characteristics TJ=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain-to-Source On-Resistance		1.0	1.3	Ω	Vgs=10V,lb=4A
Vgs(TH)	Gate Threshold Voltage	2.0		4.0	V	Vgs=Vds,Id=250µA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn-on Delay Time		25.2			Vps=325V
trise	Rise Time		16.8		nS	ID=8.0A
td(OFF)	Turn-OFF Delay Time		42.5		110	Rg=25Ω
t fall	Fall Time		16.5			(Note:3,4)

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		1250			Vgs=0V
Coss	Output Capacitance		110		pF	VDS=25V
Crss	Reverse Transfer Capacitance		12			f=1.0MHz
Qg	Total Gate Charge		23			VDS=520V
Qgs	Gate-to-Source Charge		5.9		nC	ID=8.0A VGS=10V
Qgd	Gate-to-Drain("Miller") Charge		9.46			(Note:3,4)

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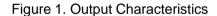
Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Is	Continuous Source Current			8.0	Α	Integral pn-diode
Ism	Maximum Pulsed Current			32.0	Α	in MOSFET
Vsd	Diode Forward Voltage			1.5	V	Is=8A,Vgs=0V
trr	Reverse Recovery Time		335		nS	Vgs=0V
Qrr	Reverse Recovery Charge		3.1		μC	Is=8A,di/dt=100A/μs

Notes:

Typical Feature curve

T_J = 25°C, unless otherwise noted



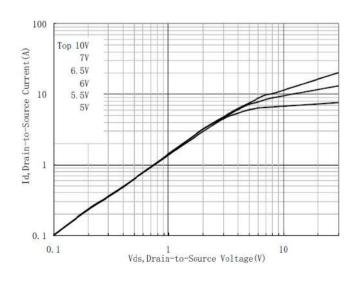
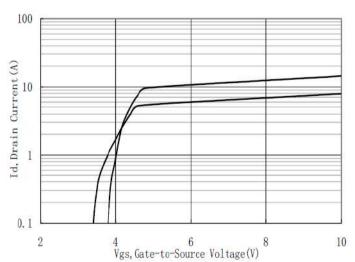


Figure 2. Typical Transfer Characteristics



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^{*1.}TJ=±25°C to +150°C.

^{*2.}Repetitive rating; pulse width limited by maximum junction temperature.

^{*3.}Pulse width≤300µs;duty cycle ≤2%.

^{*4.}Basically not affected by temperature.



Figure 3. On-Resistance versus Drain Current

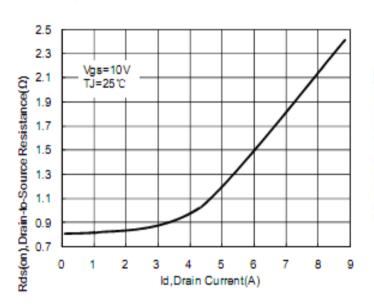


Figure 4. Diode Forward Voltage vs. Current

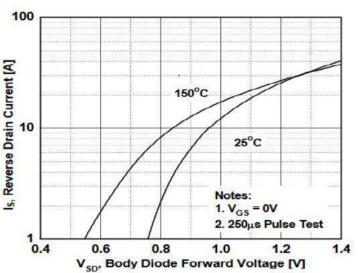


Figure 5. Capacitance vs. Drian-to-Source

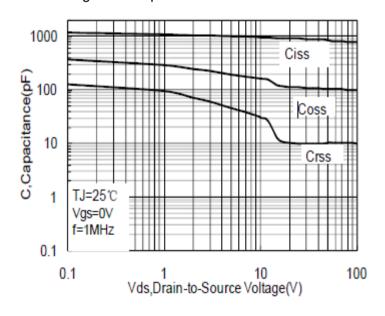
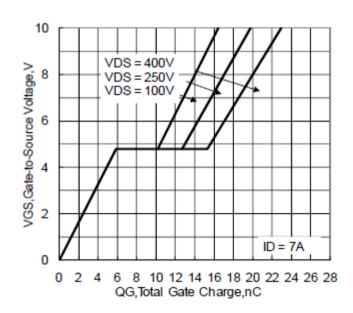


Figure 6. Gate Charge vs.Vgs



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Figure 7. Bvdss Variation with Temperature 1.2 BVDSS,(Normalized)
Drain 4o-Source Breakdown Voltage 1.15 1.1 1.05 0.95 0.9 0.85 0 25 75 100 125 150 -50 -25 50 TJ,Junction Temperature(°C)

with Temperature 2.5 2.25 PULSED TEST RDS(on), (Normalized)
Drain-to-Source On Resistance
1.25
1.0.25
1.0.25 VGS = 10V ID = 3.5A 0.5 0.25 -50 -25 25 50 75 100 125 150 TJ, Junction Temperature (°C)

Figure 8. On-Resistance Variation

Figure 9. Maximum Safe Operating Area

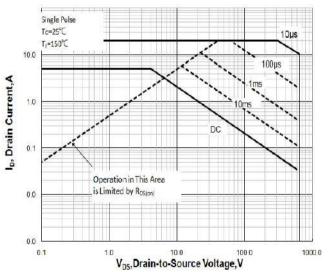
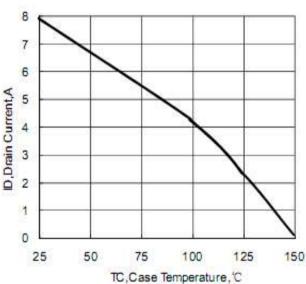


Figure 10. Maximum Continuous Drain



Test Circuits and Waveforms

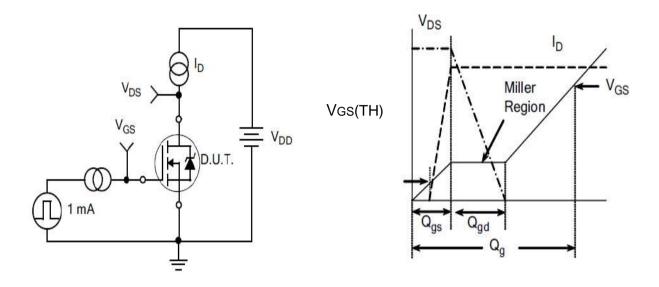


Figure11.
Gate Charge Test Circuit

Figure 12.
Gate Charge Waveform

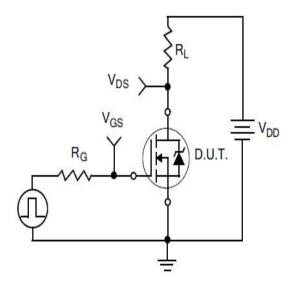


Figure 13.
Resistive Switching Test Circuit

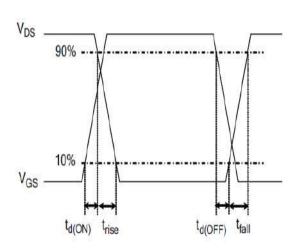


Figure 14.
Resistive Switching Waveforms

Test Circuits and Waveforms

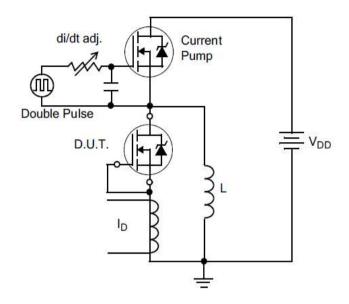


Figure 15. Diode Reverse Recovery
Test Circuit

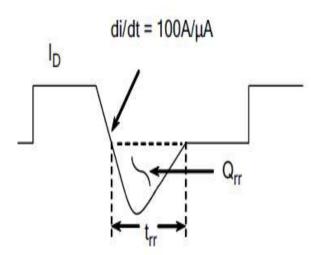


Figure 16. Diode Reverse Recovery Waveform

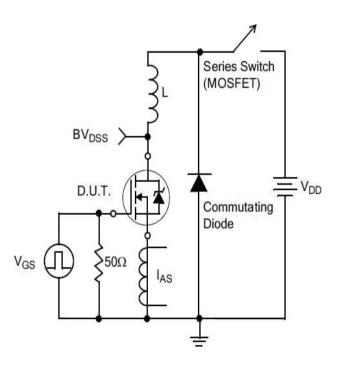
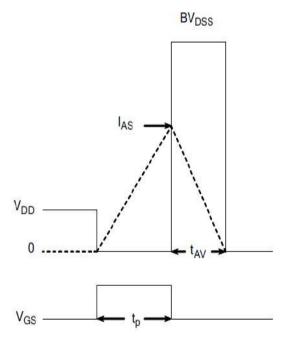


Figure 17. Unclamped Inductive Switching Test Circuit



$$EAS = \frac{IAS^2L}{2}$$

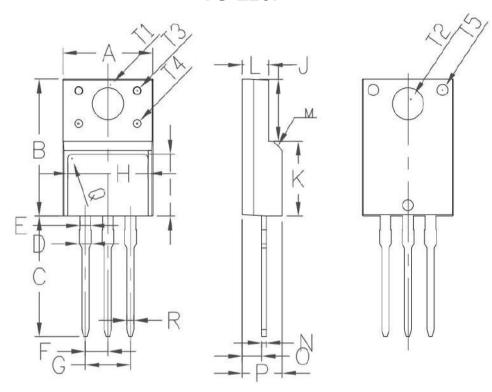
Figure 18. Unclamped Inductive Switching Waveforms



Package outline drawing

Unit: mm

TO-220F



Symbol	Min	Non	Max
A	9.96	10.16	10.36
В	15.67	15.87	16.07
С	13.14	13.34	13.54
D	1.20	1.30	1.40
E		1.20	
F		2.54	
G		5.08	
Н	7.60	7.80	8.00
I	7.10	7.30	7.50
J	6.48	6.68	6.88
K	8. 99	9.19	9.39
L	2.34	2.54	2.74
M		45°	
N	0.49	0.50	0.52
0	2.15	2.35	2,55
P	4.50	4.70	4.90
Q		0.50	
S	4°	4.5°	5°
T1		3.45	
T2		3.18	
T3		1.50	
T4		1.20	
T5		1.50	
R	0.77	0.8	0.83

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