

N Channel MOSFET

Applications:

- Adapter & Charger
- •DC-AC inverter Power
- •AC-DC Switching Power Supply
- •LED driving power

Features:

- •Low On Resistance
- •Low Gate Charge
- •Peak Current vs Pulse Width Curve
- •RoHS Compliant

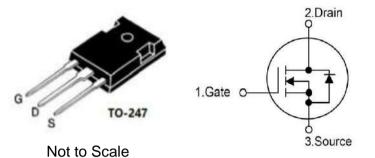
Ordering Information:

Part Number	Package	Marking
RS18N50W	TO-247	RS18N50W

P6

Lead Free Package and Finish

lD	RDS(ON)(Typ.)	VDSS
18A	0.27Ω	500V



Absolute Maximun Ratings Tc=25℃ unless otherwise specified

Symbol	Parameter	RS18N50W	Units
VDSS	Drain-to-Source Voltage (Note*1)	500	V
ID	Continuous Drain Current	18.0	
ID@ 100 ℃	Continuous Drain Current	9.0	Α
IDM	Pulsed Drain Current (Note*2)	72.0	
PD	Power Dissipation	98	W
VGS	Gate-to-Source Voltage	±30	V
EAS	Single Pulse Avalanche Engergy IAS=14A VDD=50V RG=25Ω Starting TJ=25°C	998	mJ
	Maximum Temperature for Soldering		
TL TPKG	Leads at 0.063in(1.6mm)from Case for 10 seconds	300 260	${}^{\mathbb{C}}$
	Package Body for 10 seconds		C
TJ and TSTG	Operating Junction and Storage	-55 to 150	
	Temperature Range	-33 to 130	

^{*}Drain Current Limited by Maximum Junction Temperature

Caution:Stresses greater than those listed in the "Absolute Maximum Ratings" Table may cause permanent damage to the device.

Thermal Resistance

Symbol	Parameter	RS18N50W	Units	Test Conditions
RθJC	Junction-to-Case	0.43	°C/W	Drain lead soldered to water cooled heatsink,PD adjusted for a peak junction temperature of +150℃.
RθJA	Junction-to-Ambient	41		1 cubic foot chamber,free air.

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OFF Characteristics TJ=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
BVDSS	Drain-to-source Breakdown Voltage	500			V	Vgs=0V,ID=250µA
IDSS	Drain-to-Source Leakage Current			1.0	μΑ	VDS=500V,VGS=0V
Igss	Gate-to-Source Forward Leakage			100	۸	VGS=+30V VDS=0V
1655	Gate-to-Source Reverse Leakage			-100	μΑ	Vgs=-30V Vds=0V

ON Characteristics TJ=25℃ unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
RDS(on)	Static Drain-to-Source On-Resistance		0.27	0.32	Ω	V _{GS} =10V,I _D =9A
Vgs(TH)	Gate Threshold Voltage	3.0		4.0	V	Vgs=Vds,Id=250µA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
td(ON)	Turn-on Delay Time		35			V _{DS} =250V
trise	Rise Time		50		nS	I _D =18A
td(OFF)	Turn-OFF Delay Time		180		113	$R_G = 25\Omega$
tfall	Fall Time		65			(Note:3,4)

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Ciss	Input Capacitance		2250			Vgs=0V
Coss	Output Capacitance		231		pF	VDS=25V
Crss	Reverse Transfer Capacitance		36			f=1.0MHz
Qg	Total Gate Charge		71			V _{DS} =400V
Qgs	Gate-to-Source Charge		10.0		nC	I _D =18A
Qgd	Gate-to-Drain("Miller") Charge		32			V _{GS} =10V (Note:3,4)

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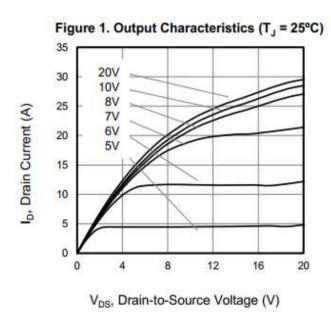


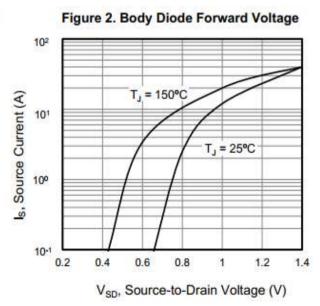
Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Is	Continuous Source Current		1	18	Α	Integral pn-diode
Ism	Maximum Pulsed Current			72	Α	in MOSFET
Vsd	Diode Forward Voltage			1.4	V	IS=18A,VGS=0V
trr	Reverse Recovery Time		570.3		nS	VGS=0V
Qrr	Reverse Recovery Charge		7.35		μC	IS=18A,di/dt=100A/μs

Notes:

Typical Feature curve





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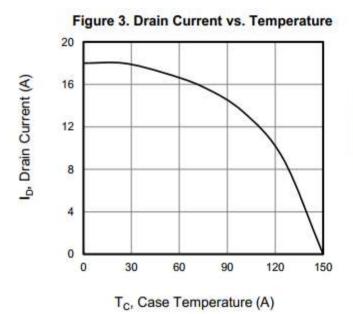
^{*1.}TJ=±25°C to +150°C.

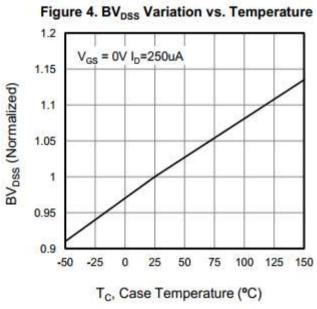
^{*2.}Repetitive rating; pulse width limited by maximum junction temperature.

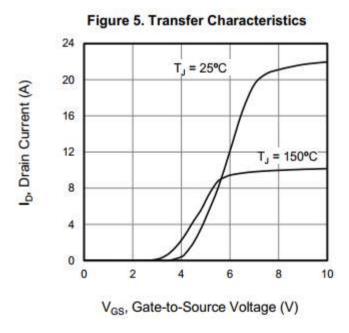
^{*3.}Pulse width≤300µs;duty cycle ≤1%.

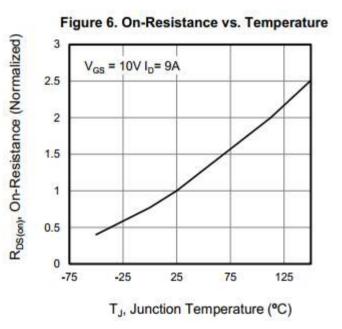
^{*4.}Basically not affected by temperature.













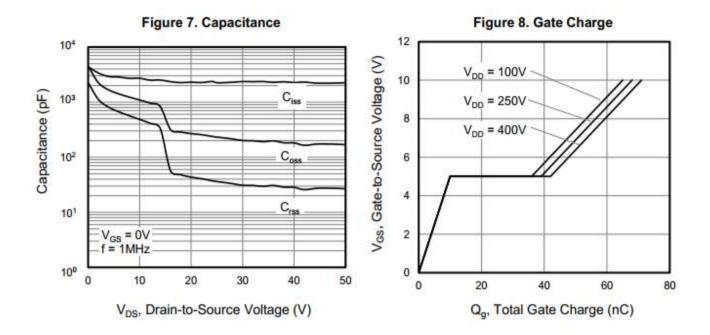
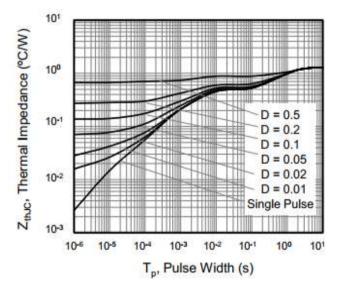


Figure 9. Transient Thermal Impedance



Test Circuits and Waveforms

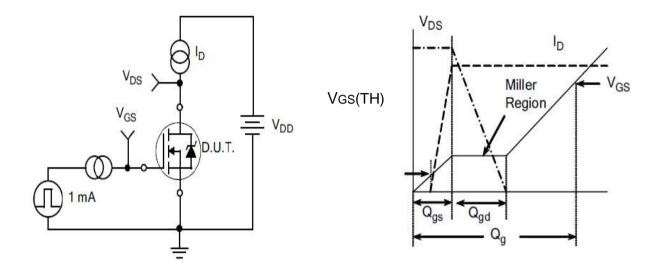


Figure 10.
Gate Charge Test Circuit

Figure11.
Gate Charge Waveform

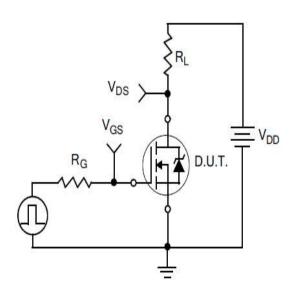


Figure 12.
Resistive Switching Test Circuit

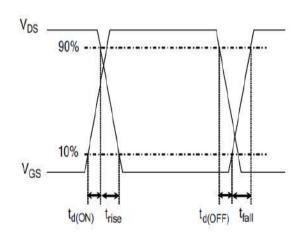


Figure 13.
Resistive Switching Waveforms

Test Circuits and Waveforms

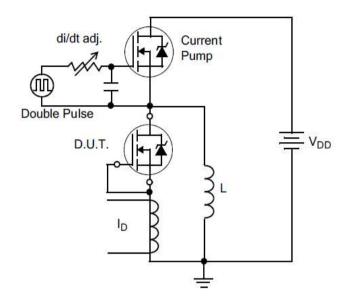


Figure 14. Diode Reverse Recovery
Test Circuit

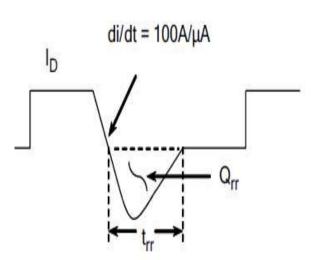


Figure 15. Diode Reverse Recovery Waveform

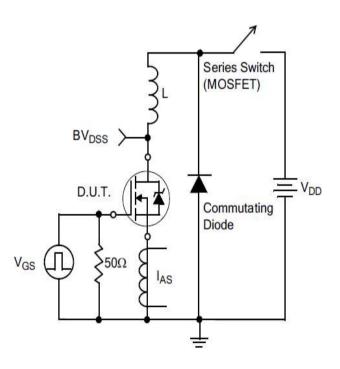
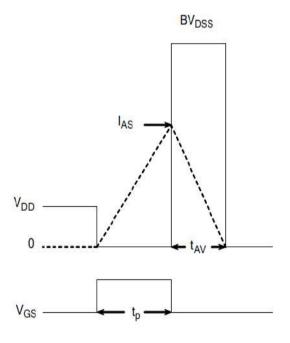


Figure 16. Unclamped Inductive Switching Test Circuit



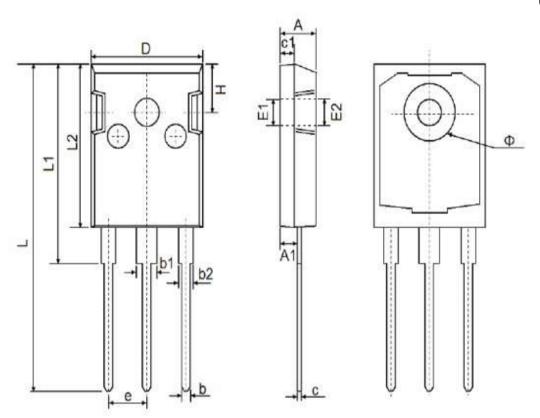
$$EAS = \frac{IAS^2L}{2}$$

Figure 17. Unclamped Inductive Switching Waveforms



Package outline drawing

Unit:mm



TO-247

Cumbal	Dimensions	In Millimeters	Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
A	4.850	5.150	0.191	0.200
A1	2.200	2.600	0.087	0.102
b	1.000	1.400	0.039	0.055
b1	2.800	3.200	0.110	0.126
b2	1.800	2.200	0.071	0.087 0.028 0.083
c	0.500	0.700	0.020	
c1	1.900	2.100	0.075	
D	15.450	15,750	0.608	0.620
E1	3.500	3.500 REF		REF
E2	3.600	3.600 REF		REF
L	40,900	41.300	1.610	1.626
L1	24.800	25.100	0.976	0.988
L2	20.300	20.600	0.799	0.811
Φ	7.100	7.300	0.280	0.287
е	5.450	TYP	0.215	TYP
н	5.980	REF	0.235 REF	



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